

Figure 1
Prior Art

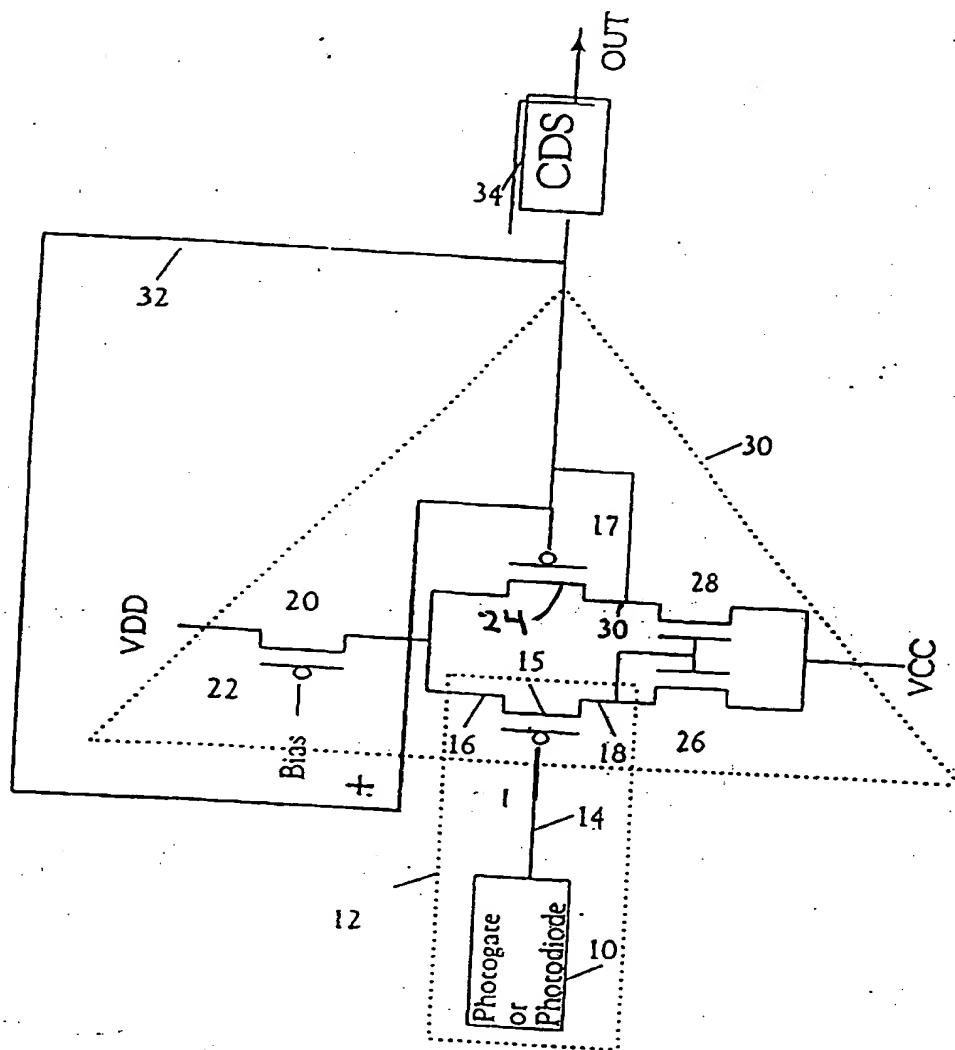


Figure 2

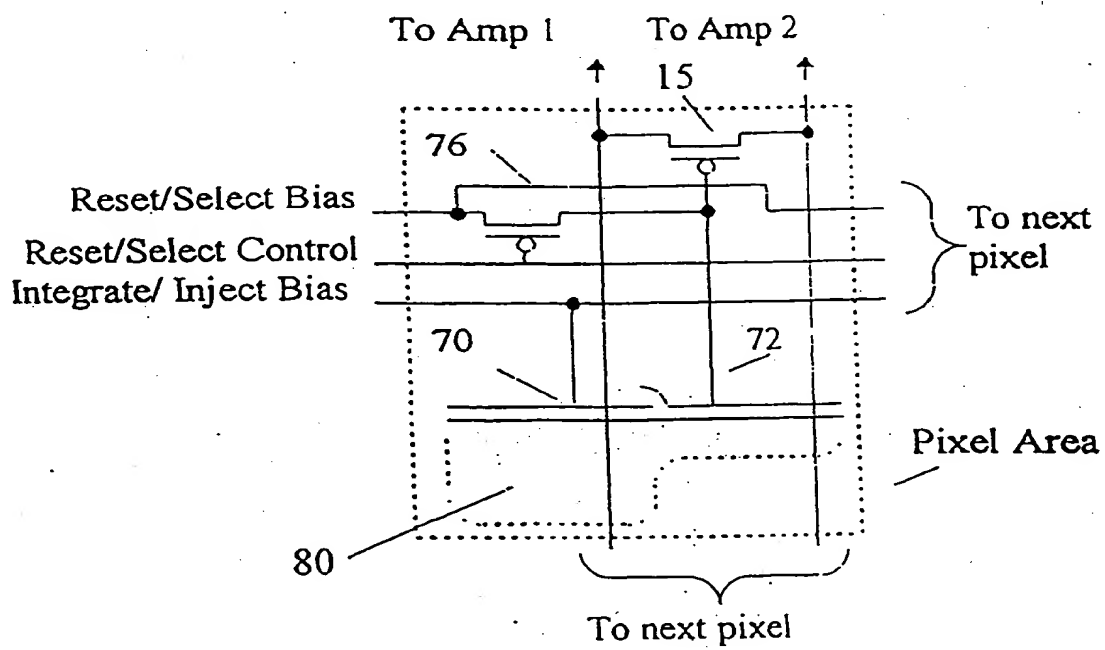


Figure 3

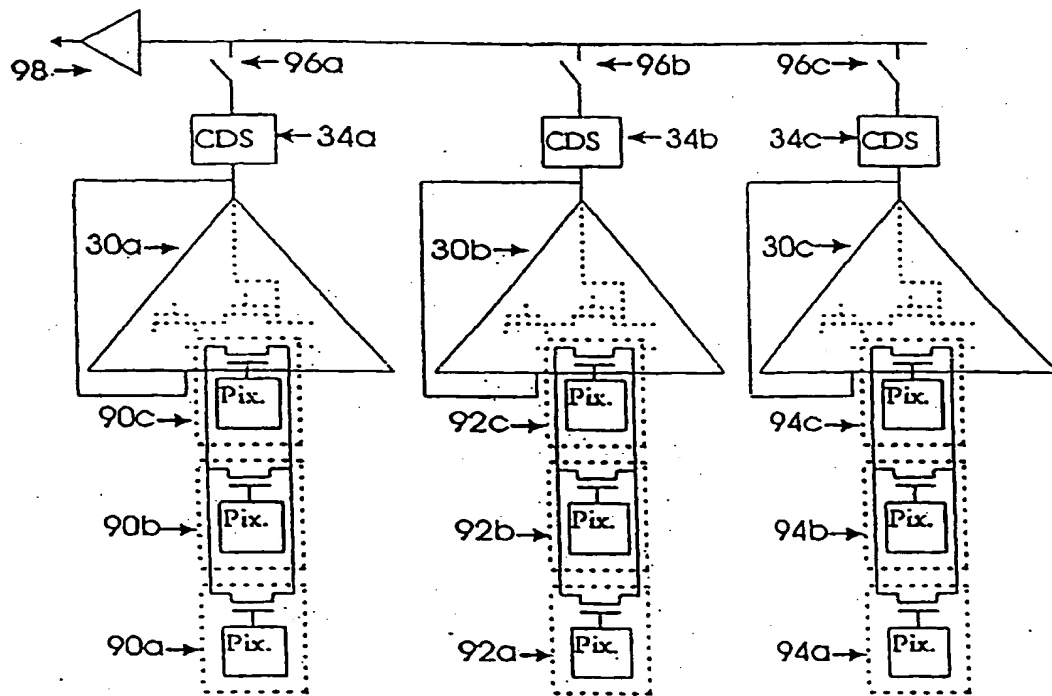


Figure 4

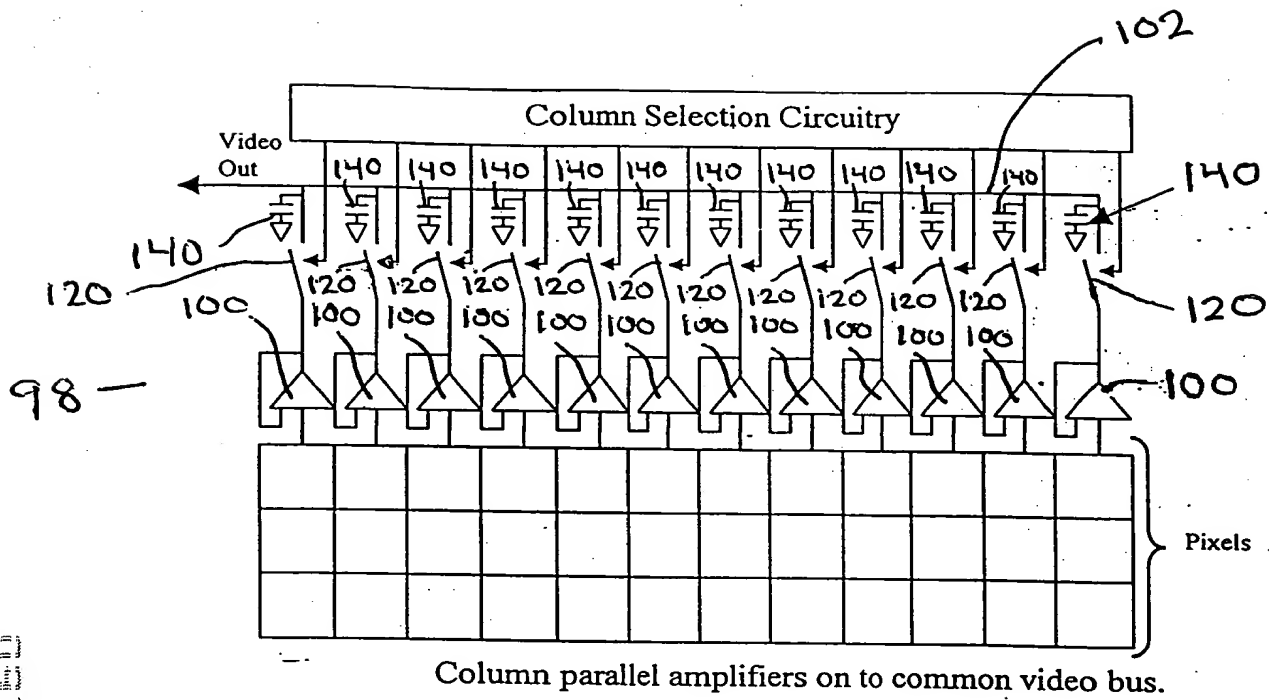


FIGURE 5

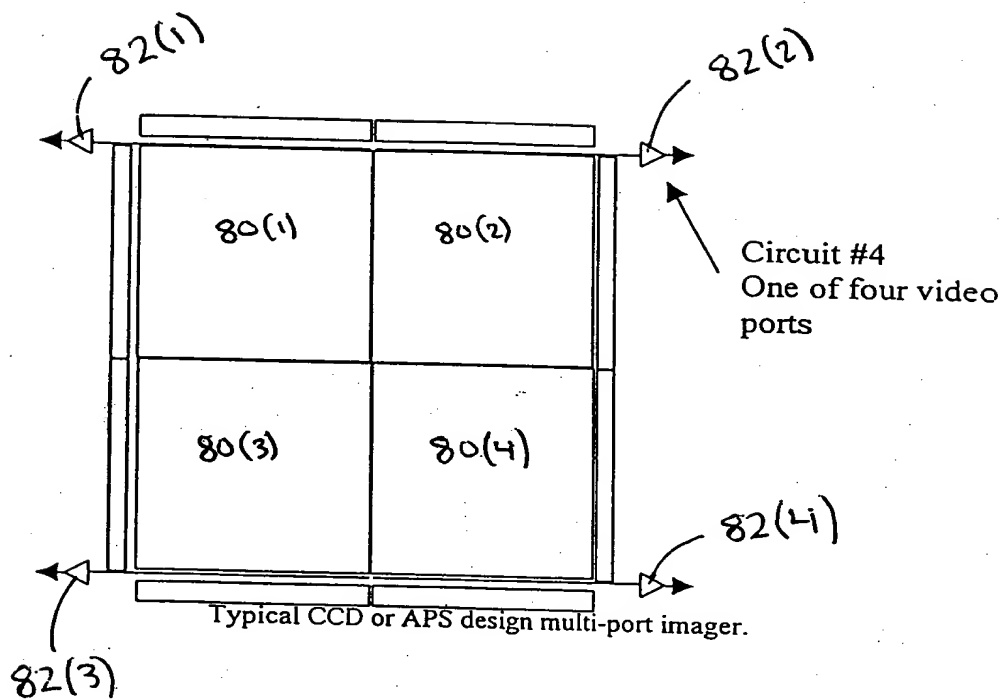


FIGURE 6

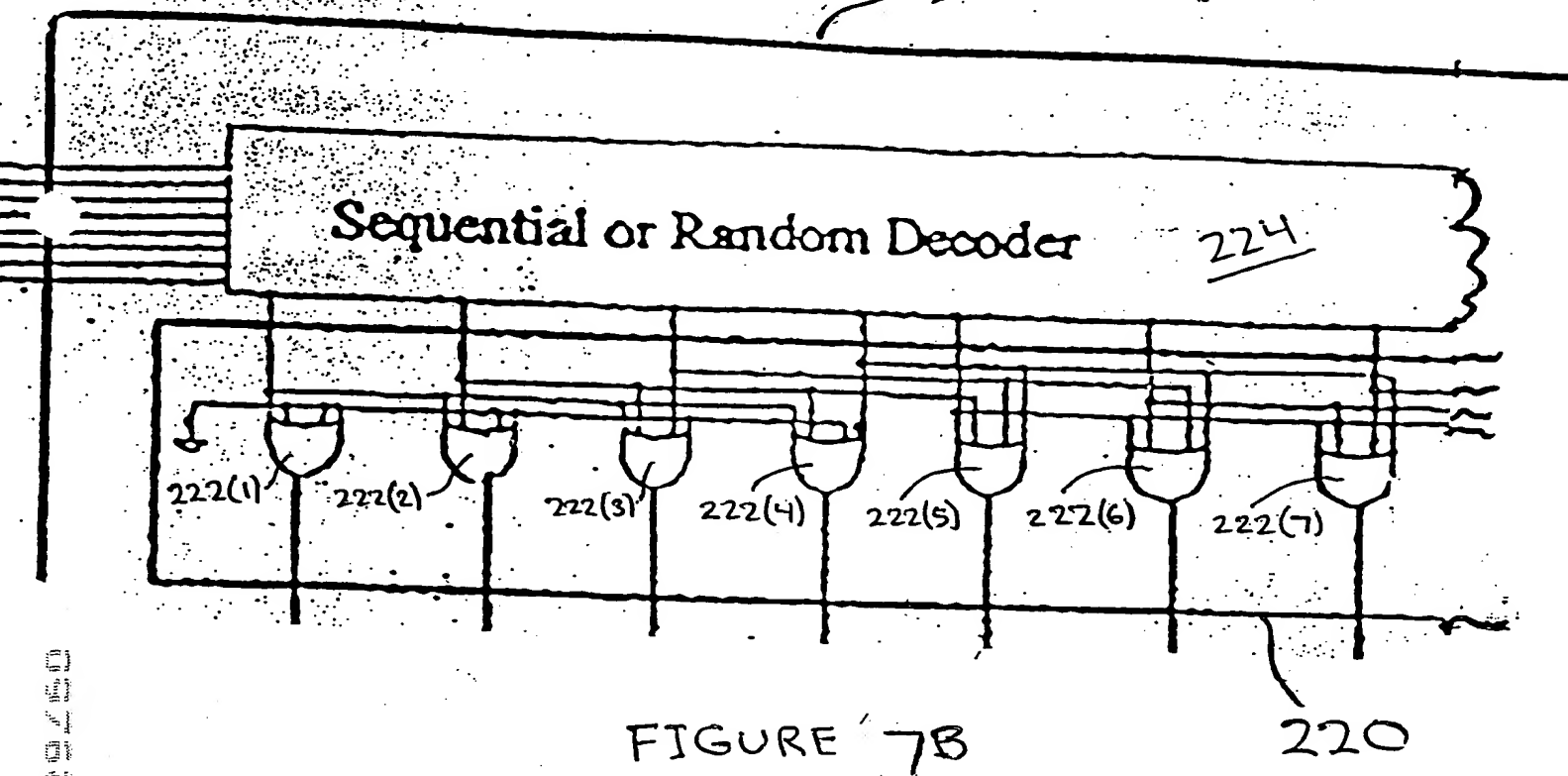


FIGURE 7B

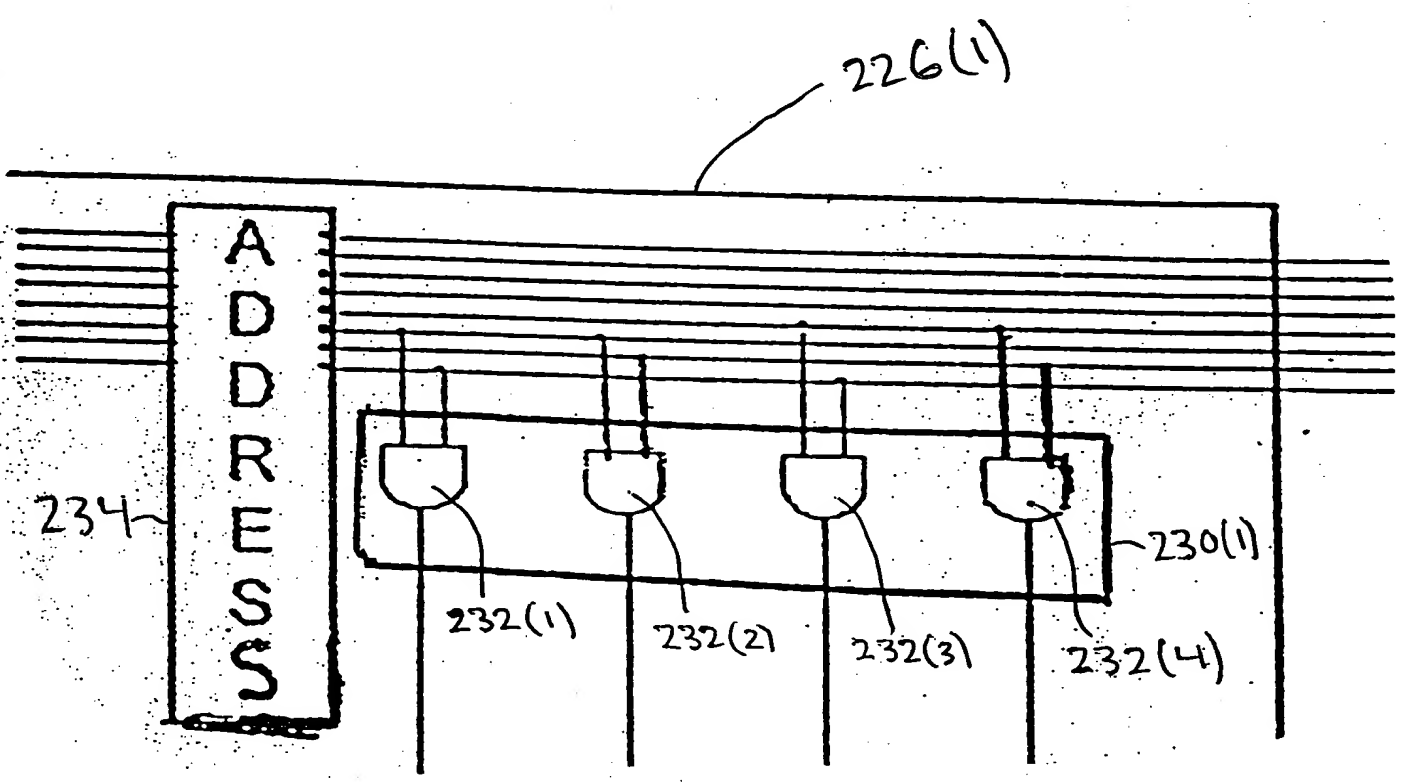


FIGURE 7C

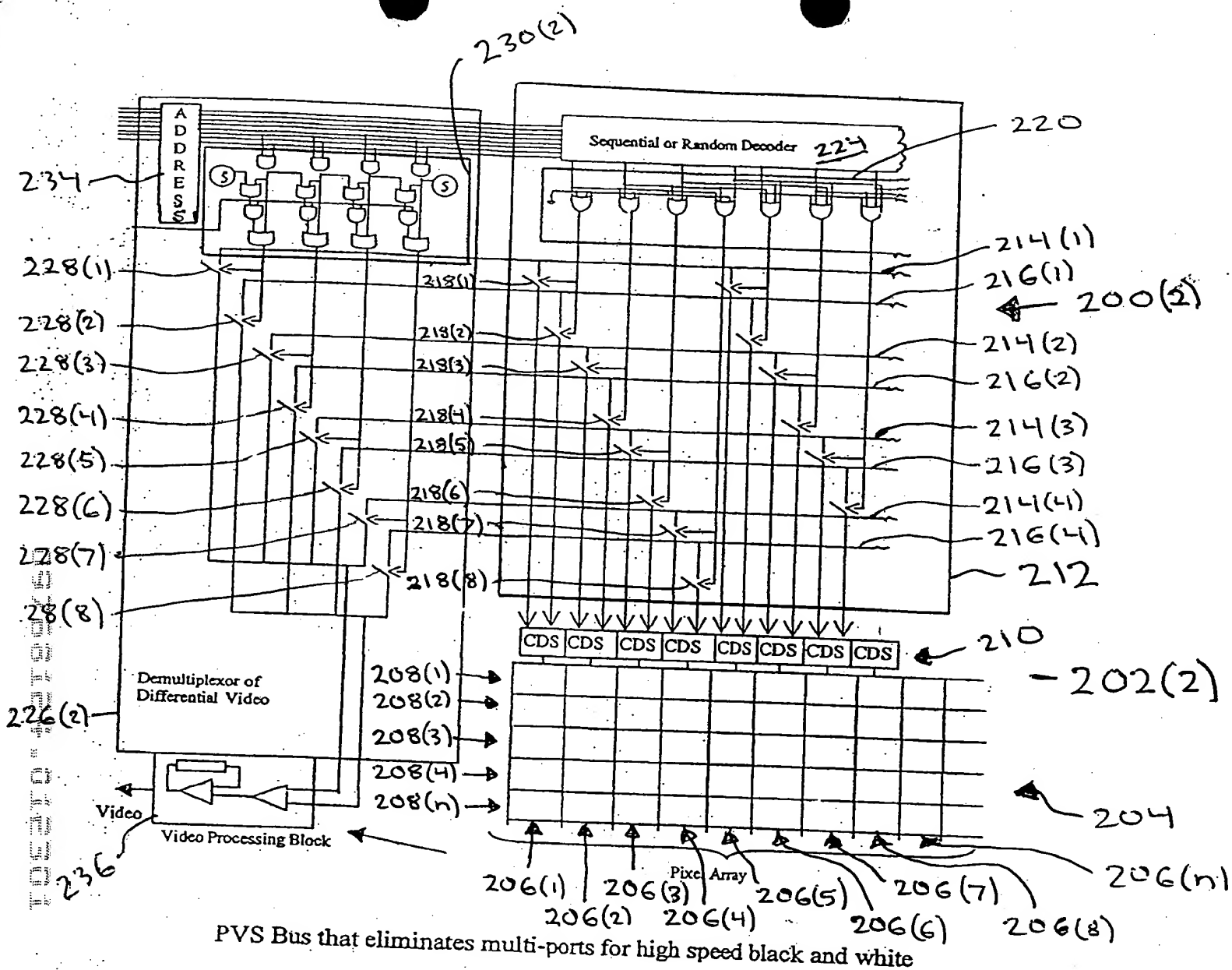


FIGURE 8A

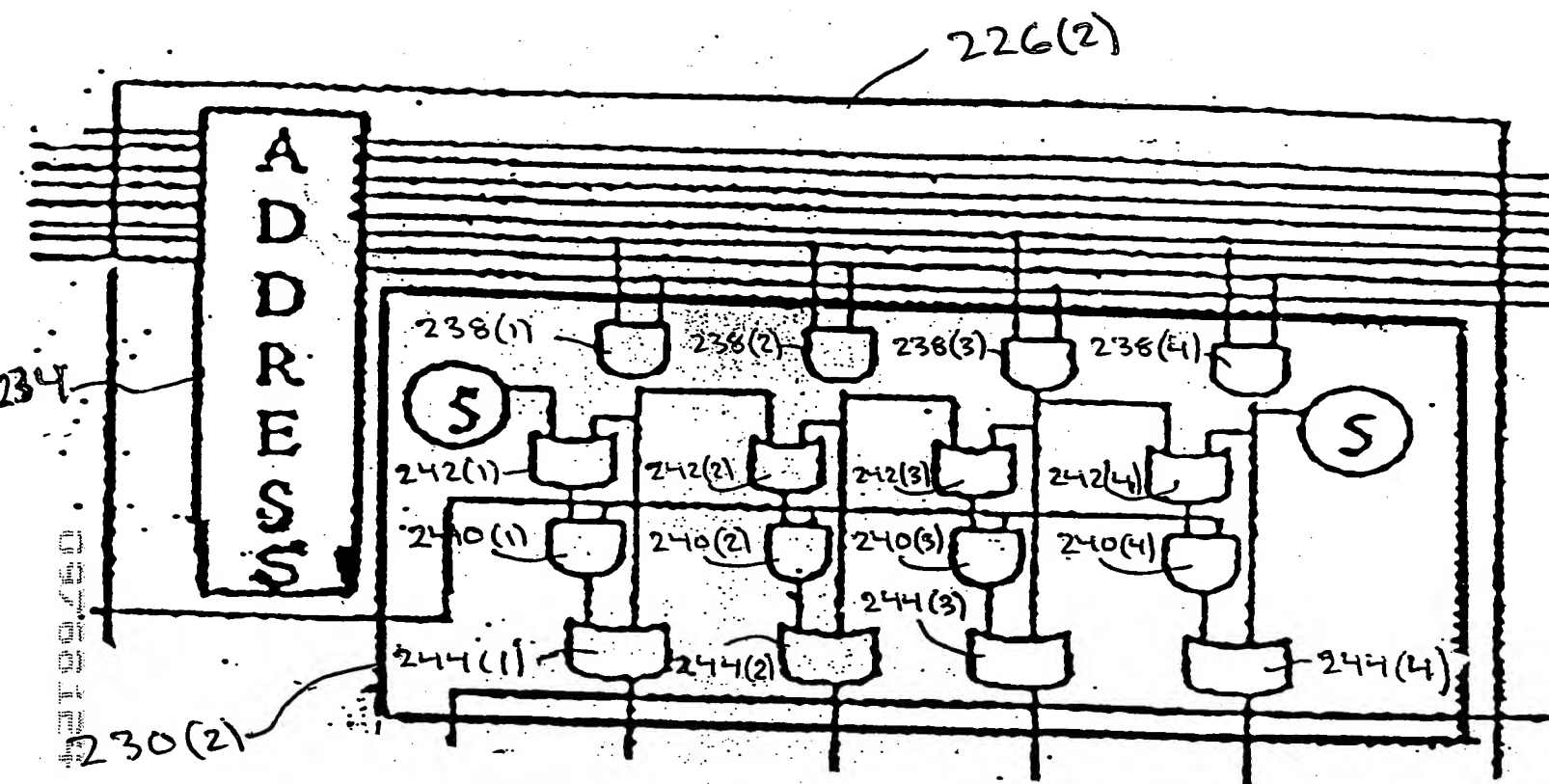


FIGURE 8B

FIG. 9 is a block diagram of a PVS Buss with Pixel reordering Example for separate Red Green and Blue processing Out

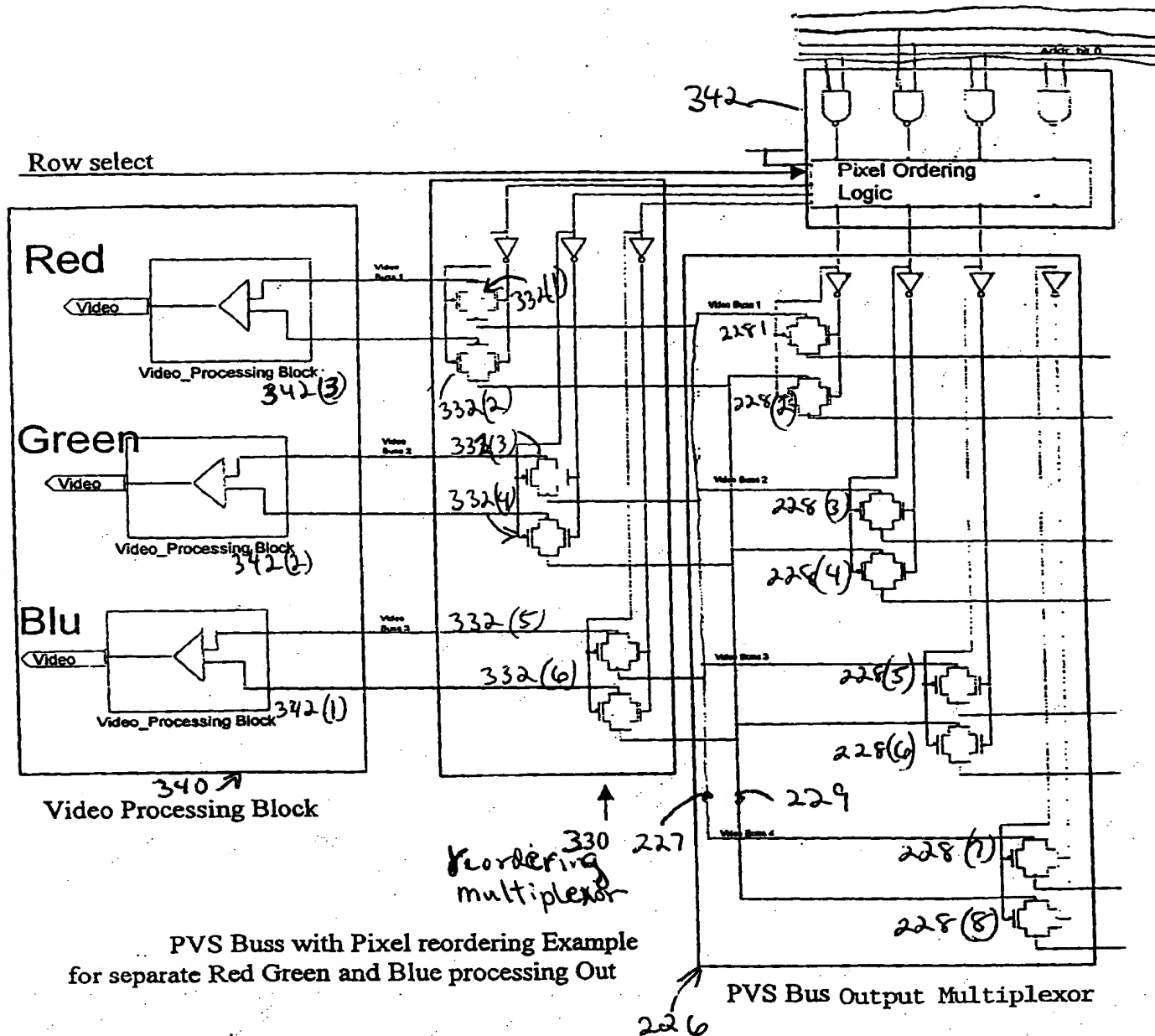


FIGURE 9